

Reg.No:		:]		
9	SID		ГНТ	NST	ITU	TE (OF E	NGI	NEF	RIN	IG 8	TECHNOLOGY:: PUTTUR		
•						12	(A	UTC	DNO	MO	US)			
	B. '	Fech II	I Yea	ar I S	Sem	ester	Reg	ular	Exa	mina N LC	ntion	s November/December 2018		
						LIN	EAF	EIC (E	API CE.E	EE)	AII	UNS		
Time:	3 ho	ours						(—	,-	/		Max Marks:	:60	
					(A	nswe	er all	Five	Uni	ts 5 x	x 12 :	= 60 Marks)		
1	_	C-11	- 4 - 41		1:6	•	. .		UNI	T-I		· · · · · · · · · · · · · · · · · · ·		
1	а	output differential amplifier?												
	b Explain how the constant current bias circuit is replaced by the current								eplaced by the current mirror					
		circuit	?									······································	5M	
									0	R				
2	a	Calcul Outpu	ate th it dif	ne an feren	nplif tial a	icatio ampl	on fao ifier?	ctor f	for A	C sig	gnal i	input in single input balanced	7M	
	b	Discus	ss the	DC	char	acter	ristics	s of a	ın Ol	P-AN	1P in	detail	5M	
									UNI	T-II				
3	a	Expla	in ir	ı det	ail	abou	t ext	erna	l fre	equen	icy o	compensation techniques with	7M	
	h	SKetch	es. n the	inte	mal	com	nenca	ting	tech	niaue	`		5M	
	U	Елріаі	II UIC	me	mai	comj	201150	ung	0	R	•		5111	
4	a	Calcul amplif	ate th	ne inj	out r	esist	ance	and	outpu	it res	istan	ce for a voltage shunt feedback	7M	
	b	Explain	n the	term	"Sl	ew R	ate"	and o	deriv	e the	exp	ression for it.	5M	
								1	UNI	Г-III				
5	a	Explai diagra	n an m.	d der	ive	the e	xpres	ssion	for	3 inp	out s	umming amplifiers with circuit	7M	
	b	Explair circuit	n the s	frequ	ienc	y res	ponse	es of	idea	1 & p	oracti	ical integrator and differentiator	5M	
									0	R				
6	a	Draw	the ci	ircuit	dia	gram	of th	e ins	strun	nenta	tion	amplifier and derive the gain.	7M	
	b	Design the cire	1 a se cuit c	cond liagra	ord am.	er lo	w pas	ss fil	ter fo	or a c	utofi	f frequency of 100 Hz and draw	5M	
-		D	.1	• • ,	1.		(D		UNI'.	Γ-IV	•1	1. 11		
7	a	for ite	frequ	ircuit	aiag	gram	OI K	c ph	lase s	snift (JSC11	lator and derive the expression	7M	
	h	Explai	n the	com	par	tor a	nd ze	ero ci	rossi	ng de	etecto	or.	5M	
				- 511	r are				0	R			~	
8	a	Explai	n in '	whic	h the	e 555	time	r car	n be i	used	as A	stable multivibrator.	7M	
	b	Config	gure a	a 555	tim	er as	a Scl	hmit	t trig	ger a	nd ex	xplain.	5M	



UNIT-V

9	a	Draw and explain successive approximation type ADC.							
	b	Explain about the sample and hold circuits.							
		OR							
10	a	Draw the circuit diagram of Dual Slope ADC and explain its working with neat sketches	7M						
	b	Explain about ladder type DAC.	5M						

*** END ***